

# DATA SHEET

**LM139/239/239A/339/339A/  
LM2901/MC3302**  
Quad voltage comparator

Product data  
Supersedes data of 2002 Jul 12

2002 Sep 17

# Quad voltage comparator

## LM139/239/239A/339/339A/ LM2901/MC3302

### DESCRIPTION

The LM139 series consists of four independent precision voltage comparators, with an offset voltage specification as low as 2.0 mV max for each comparator, which were designed specifically to operate from a single power supply over a wide range of voltages. Operation from split power supplies is also possible and the low power supply current drain is independent of the magnitude of the power supply voltage. These comparators also have a unique characteristic in that the input common-mode voltage range includes ground, even though they are operated from a single power supply voltage.

The LM139 series was designed to directly interface with TTL and CMOS. When operated from both plus and minus power supplies, the LM139 series will directly interface with MOS logic where their low power drain is a distinct advantage over standard comparators.

### FEATURES

- Wide single supply voltage range 2.0 V<sub>DC</sub> to 32 V<sub>DC</sub> or dual supplies ±1.0 V<sub>DC</sub> to ±16 V<sub>DC</sub>
- Very low supply current drain (0.8 mA) independent of supply voltage (1.0 mW/comparator at 5.0 V<sub>DC</sub>)
- Low input biasing current 25 nA
- Low input offset current ±5 nA and offset voltage
- Input common-mode voltage range includes ground
- Differential input voltage range equal to the power supply voltage
- Low output 250 mV at 4 mA saturation voltage
- Output voltage compatible with TTL, DTL, ECL, MOS and CMOS logic systems

### APPLICATIONS

- A/D converters
- Wide range VCO
- MOS clock generator
- High voltage logic gate
- Multivibrators

### PIN CONFIGURATION

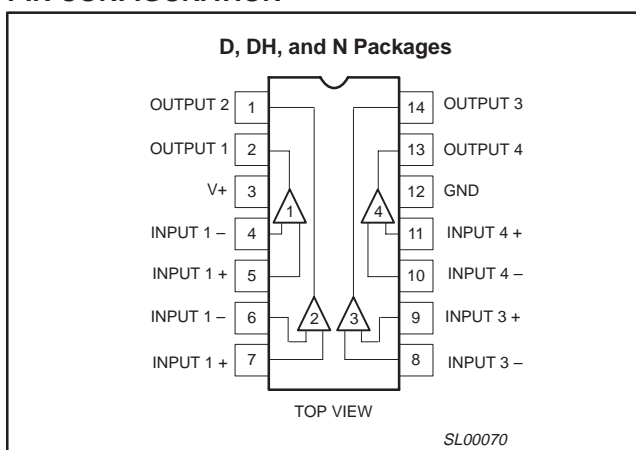


Figure 1. Pin Configuration

### EQUIVALENT CIRCUIT

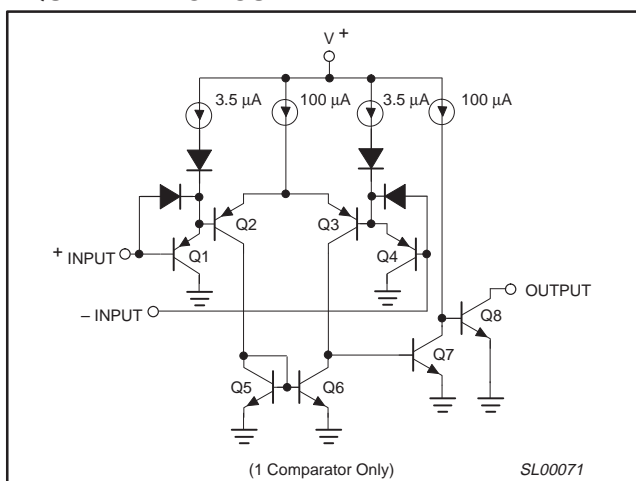


Figure 2. Equivalent Circuit

### ORDERING INFORMATION

| DESCRIPTION  | TEMPERATURE RANGE | ORDER CODE | DWG #    |
|--|-------------------|------------|----------|
| 14-Pin Plastic Dual In-Line Package (DIP)                | -55 °C to +125 °C | LM139N     | SOT27-1  |
| 14-Pin Plastic Small Outline (SO) Package                | -25 °C to +85 °C  | LM239D     | SOT108-1 |
| 14-Pin Plastic Dual In-Line Package (DIP)                | -25 °C to +85 °C  | LM239N     | SOT27-1  |
| 14-Pin Plastic Dual In-Line Package (DIP)                | -25 °C to +85 °C  | LM239AN    | SOT27-1  |
| 14-Pin Plastic Small Outline (SO) Package                | -40 °C to +125 °C | LM2901D    | SOT108-1 |
| 14-Pin Plastic Dual In-Line Package (DIP)                | -40 °C to +125 °C | LM2901N    | SOT27-1  |
| 14-Pin Plastic Thin Shrink Small Outline Package (TSSOP) | -40 °C to +125 °C | LM2901DH   | SOT402-1 |
| 14-Pin Plastic Small Outline (SO) Package                | 0 °C to +70 °C    | LM339AD    | SOT108-1 |
| 14-Pin Plastic Dual In-Line Package (DIP)                | 0 °C to +70 °C    | LM339AN    | SOT27-1  |
| 14-Pin Plastic Small Outline (SO) Package                | 0 °C to +70 °C    | LM339D     | SOT108-1 |
| 14-Pin Plastic Thin Shrink Small Outline Package (TSSOP) | 0 °C to +70 °C    | LM339DH    | SOT402-1 |
| 14-Pin Plastic Dual In-Line Package (DIP)                | 0 °C to +70 °C    | LM339N     | SOT27-1  |
| 14-Pin Plastic Small Outline (SO) Package                | -40 °C to +85 °C  | MC3302D    | SOT108-1 |
| 14-Pin Plastic Dual In-Line Package (DIP)                | -40 °C to +85 °C  | MC3302N    | SOT27-1  |

## Quad voltage comparator

LM139/239/239A/339/339A/  
LM2901/MC3302

## ABSOLUTE MAXIMUM RATINGS

| SYMBOL     | PARAMETER  | RATING           | UNIT             |
|------------|--|------------------|------------------|
| $V_{CC}$   | $V_{CC}$ supply voltage  | 32 or $\pm 16$   | $V_{DC}$         |
| $V_{DIFF}$ | Differential input voltage   | 32               | $V_{DC}$         |
| $V_{IN}$   | Input voltage  | -0.3 to +32      | $V_{DC}$         |
| $P_D$      | Maximum power dissipation, $T_{amb} = 25\text{ }^\circ\text{C}$ (still-air) <sup>1</sup> |                  |                  |
|            | N package  | 1420             | mW               |
|            | D package  | 1040             | mW               |
|            | DH package   | 762              | mW               |
|            | Output short-circuit to ground <sup>2</sup>  | Continuous       |                  |
| $I_{IN}$   | Input current ( $V_{IN} < -0.3 V_{DC}$ ) <sup>3</sup>                                    | 50               | mA               |
| $T_{amb}$  | Operating temperature range  |                  |                  |
|            | LM139  | -55 to +125      | $^\circ\text{C}$ |
|            | LM239/239A   | -25 to +85       | $^\circ\text{C}$ |
|            | LM339/339A   | 0 to +70         | $^\circ\text{C}$ |
|            | LM2901   | -40 to +125      | $^\circ\text{C}$ |
| MC3302     | -40 to +85   | $^\circ\text{C}$ |                  |
| $T_{stg}$  | Storage temperature range  | -65 to +150      | $^\circ\text{C}$ |
| $T_{sld}$  | Lead soldering temperature (10 sec max)  | 230              | $^\circ\text{C}$ |

## NOTES:

- Derate above 25  $^\circ\text{C}$ , at the following rates:  
 N Package at 11.4 mW/ $^\circ\text{C}$   
 D Package at 8.3 mW/ $^\circ\text{C}$   
 DH Package at 6.1 mW/ $^\circ\text{C}$
- Short circuits from the output to  $V_+$  can cause excessive heating and eventual destruction. The maximum output current is approximately 20 mA independent of the magnitude of  $V_+$ .
- This input current will only exist when the voltage at any of the input leads is driven negative. It is due to the collector-base junction of the input PNP transistors becoming forward biased and thereby acting as input diode clamps. In addition to this diode action, there is also lateral NPN parasitic transistor action on the IC chip. This transistor action can cause the output voltages of the comparators to go to the  $V_+$  voltage level (or to ground for a large overdrive) for the time duration that an input is driven negative. This is not destructive and normal output states will reestablish when the input voltage, which was negative, again returns to a value greater than  $-0.3 V_{DC}$ .

## Quad voltage comparator

LM139/239/239A/339/339A/  
LM2901/MC3302**DC AND AC ELECTRICAL CHARACTERISTICS**

$V_+ = 5 V_{DC}$ ; LM139:  $-55\text{ °C} \leq T_{amb} \leq +125\text{ °C}$ ; LM239/239A:  $-25\text{ °C} \leq T_{amb} \leq +85\text{ °C}$ ; LM339/339A:  $0\text{ °C} \leq T_{amb} \leq +70\text{ °C}$ ;  
LM2901:  $-40\text{ °C} \leq T_{amb} \leq +125\text{ °C}$ ; MC3302:  $-40\text{ °C} \leq T_{amb} \leq +85\text{ °C}$ , unless otherwise specified.

| SYMBOL     | PARAMETER                                    | TEST CONDITIONS   | LM239A/339A |           |                       | UNIT          |
|------------|--|---|-------------|-----------|-----------------------|---------------|
|            |  |   | Min         | Typ       | Max                   |               |
| $V_{OS}$   | Input offset voltage <sup>2</sup>            | $T_{amb} = 25\text{ °C}$  |             | $\pm 1.0$ | $\pm 2.0$             | mV            |
|            |  | Over temp.  |             |           | $\pm 4.0$             | mV            |
| $V_{CM}$   | Input common-mode voltage range <sup>3</sup> | $T_{amb} = 25\text{ °C}$  | 0           |           | $V_+ - 1.5$           | V             |
|            |  | Over temp.  | 0           |           | $V_+ - 2.0$           | V             |
| $V_{IDR}$  | Differential input voltage <sup>1</sup>      | Keep all $V_{IN} \geq 0 V_{DC}$ (or $V_-$ if needed)  |             |           | $V_+$                 | V             |
| $I_{BIAS}$ | Input bias current <sup>4</sup>              | $I_{IN(+)}$ or $I_{IN(-)}$ with output in linear range<br>$T_{amb} = 25\text{ °C}$<br>Over temp.  |             | 25        | 250<br>400            | nA<br>nA      |
| $I_{OS}$   | Input offset current                         | $I_{IN(+)} - I_{IN(-)}$<br>$T_{amb} = 25\text{ °C}$<br>Over temp.   |             | $\pm 5.0$ | $\pm 50$<br>$\pm 150$ | nA<br>nA      |
| $I_{OL}$   | Output sink current                          | $V_{IN(-)} \geq 1 V_{DC}$ ; $V_{IN(+)} = 0$ ; $V_O \leq 1.5 V_{DC}$ ;<br>$T_{amb} = 25\text{ °C}$   | 6.0         | 16        |                       | mA            |
|            | Output leakage current                       | $V_{IN(+)} \geq 1 V_{DC}$ ; $V_{IN(-)} = 0$<br>$V_O = 5 V_{DC}$ ; $T_{amb} = 25\text{ °C}$<br>$V_O = 30 V_{DC}$ ; over temp.                |             | 0.1       | 1.0                   | nA<br>$\mu A$ |
| $I_{CC}$   | Supply current                               | $R_L = \infty$ on comparators; $V_+ = 30 V$ ;<br>$T_{amb} = 25\text{ °C}$   |             | 0.8       | 2.0                   | mA            |
| $A_V$      | Voltage gain                                 | $R_L \geq 15\text{ k}\Omega$ ; $V_+ = 15 V_{DC}$  | 50          | 200       |                       | V/mV          |
| $V_{OL}$   | Saturation voltage                           | $V_{IN(-)} \geq 1 V_{DC}$ ; $V_{IN(+)} = 0$ ; $I_{SINK} \leq 4\text{ mA}$<br>$T_{amb} = 25\text{ °C}$<br>Over temp.                         |             | 250       | 400<br>700            | mV<br>mV      |
| $t_{LSR}$  | Large-signal response time                   | $V_{IN} = \text{TTL logic swing}$ ; $V_{REF} = 1.4 V_{DC}$ ; $V_{RL} = 5 V_{DC}$ ;<br>$R_L = 5.1\text{ k}\Omega$ ; $T_{amb} = 25\text{ °C}$ |             | 300       |                       | ns            |
| $t_R$      | Response time <sup>5</sup>                   | $V_{RL} = 5 V_{DC}$ ; $R_L = 5.1\text{ k}\Omega$ ; $T_{amb} = 25\text{ °C}$   |             | 1.3       |                       | $\mu s$       |

See notes at the end of the Electrical Characteristics.

## Quad voltage comparator

LM139/239/239A/339/339A/  
LM2901/MC3302**DC AND AC ELECTRICAL CHARACTERISTICS** (continued)

$V_+ = 5 V_{DC}$ ; LM139:  $-55\text{ °C} \leq T_{amb} \leq +125\text{ °C}$ ; LM239/239A:  $-25\text{ °C} \leq T_{amb} \leq +85\text{ °C}$ ; LM339/339A:  $0\text{ °C} \leq T_{amb} \leq +70\text{ °C}$ ;  
LM2901:  $-40\text{ °C} \leq T_{amb} \leq +125\text{ °C}$ ; MC3302:  $-40\text{ °C} \leq T_{amb} \leq +85\text{ °C}$ , unless otherwise specified.

| SYMBOL     | PARAMETER                                    | TEST CONDITIONS  | LM139 |           |                       | LM239/339 |           |                       | UNIT          |
|------------|--|--|-------|-----------|-----------------------|-----------|-----------|-----------------------|---------------|
|            |  |  | Min   | Typ       | Max                   | Min       | Typ       | Max                   |               |
| $V_{OS}$   | Input offset voltage <sup>2</sup>            | $T_{amb} = 25\text{ °C}$   |       | $\pm 2.0$ | $\pm 5.0$             |           | $\pm 2.0$ | $\pm 5.0$             | mV            |
|            |  | Over temp.   |       |           | $\pm 9.0$             |           |           | $\pm 9.0$             | mV            |
| $V_{CM}$   | Input common-mode voltage range <sup>3</sup> | $T_{amb} = 25\text{ °C}$   | 0     |           | $V_+ - 1.5$           | 0         |           | $V_+ - 1.5$           | V             |
|            |  | Over temp.   | 0     |           | $V_+ - 2.0$           | 0         |           | $V_+ - 2.0$           | V             |
| $V_{IDR}$  | Differential input voltage <sup>1</sup>      | Keep all $V_{IN} \geq 0 V_{DC}$ (or $V_-$ if needed)   |       |           | $V_+$                 |           |           | $V_+$                 | V             |
| $I_{BIAS}$ | Input bias current <sup>4</sup>              | $I_{IN(+)}$ or $I_{IN(-)}$ with output in linear range   |       | 25        | 100                   |           | 25        | 250                   | nA            |
|            |  | $T_{amb} = 25\text{ °C}$<br>Over temp.   |       |           | 300                   |           |           | 400                   | nA            |
| $I_{OS}$   | Input offset current                         | $I_{IN(+)} - I_{IN(-)}$<br>$T_{amb} = 25\text{ °C}$<br>Over temp.  |       | $\pm 3.0$ | $\pm 25$<br>$\pm 100$ |           | $\pm 5.0$ | $\pm 50$<br>$\pm 150$ | nA<br>nA      |
| $I_{OL}$   | Output sink current                          | $V_{IN(-)} \geq 1 V_{DC}$ ; $V_{IN(+)} = 0$ ;<br>$V_O \leq 1.5 V_{DC}$ ;<br>$T_{amb} = 25\text{ °C}$   | 6.0   | 16        |                       | 6.0       | 16        |                       | mA            |
|            | Output leakage current                       | $V_{IN(+)} \geq 1 V_{DC}$ ; $V_{IN(-)} = 0$<br>$V_O = 5 V_{DC}$ ; $T_{amb} = 25\text{ °C}$<br>$V_O = 30 V_{DC}$ ; Over temp.                   |       | 0.1       | 1.0                   |           | 0.1       | 1.0                   | nA<br>$\mu A$ |
| $I_{CC}$   | Supply current                               | $R_L = \infty$ on comparators; $V_+ = 30 V$ ;<br>$T_{amb} = 25\text{ °C}$  |       | 0.8       | 2.0                   |           | 0.8       | 2.0                   | mA            |
| $A_V$      | Voltage gain                                 | $R_L \geq 15\text{ k}\Omega$ ; $V_+ = 15 V_{DC}$   | 50    | 200       |                       | 50        | 200       |                       | V/mV          |
| $V_{OL}$   | Saturation voltage                           | $V_{IN(-)} \geq 1 V_{DC}$ ; $V_{IN(+)} = 0$ ; $I_{SINK} \leq 4\text{ mA}$<br>$T_{amb} = 25\text{ °C}$<br>Over temp.                            |       | 250       | 400<br>700            |           | 250       | 400<br>700            | mV<br>mV      |
|            |  |  |       |           |                       |           |           |                       |               |
| $t_{LSR}$  | Large-signal response time                   | $V_{IN} = \text{TTL logic swing}$ ; $V_{REF} = 1.4 V_{DC}$ ;<br>$V_{RL} = 5 V_{DC}$ ; $R_L = 5.1\text{ k}\Omega$ ;<br>$T_{amb} = 25\text{ °C}$ |       | 300       |                       |           | 300       |                       | ns            |
| $t_R$      | Response time <sup>5</sup>                   | $V_{RL} = 5 V_{DC}$ ; $R_L = 5.1\text{ k}\Omega$ ;<br>$T_{amb} = 25\text{ °C}$   |       | 1.3       |                       |           | 1.3       |                       | $\mu s$       |

See notes on following page.

## Quad voltage comparator

LM139/239/239A/339/339A/  
LM2901/MC3302**DC AND AC ELECTRICAL CHARACTERISTICS** (continued)

$V_+ = 5 V_{DC}$ ; LM139:  $-55\text{ }^\circ\text{C} \leq T_{amb} \leq +125\text{ }^\circ\text{C}$ ; LM239/239A:  $-25\text{ }^\circ\text{C} \leq T_{amb} \leq +85\text{ }^\circ\text{C}$ ; LM339/339A:  $0\text{ }^\circ\text{C} \leq T_{amb} \leq +70\text{ }^\circ\text{C}$ ;  
LM2901:  $-40\text{ }^\circ\text{C} \leq T_{amb} \leq +125\text{ }^\circ\text{C}$ ; MC3302:  $-40\text{ }^\circ\text{C} \leq T_{amb} \leq +85\text{ }^\circ\text{C}$ , unless otherwise specified.

| SYMBOL     | PARAMETER                                    | TEST CONDITIONS  | LM2901 |                     |                       | MC3302 |           |                        | UNIT                |
|------------|--|--|--------|---------------------|-----------------------|--------|-----------|------------------------|---------------------|
|            |  |  | Min    | Typ                 | Max                   | Min    | Typ       | Max                    |                     |
| $V_{OS}$   | Input offset voltage <sup>2</sup>            | $T_{amb} = 25\text{ }^\circ\text{C}$   |        | $\pm 2.0$           | $\pm 7.0$             |        | $\pm 3.0$ | $\pm 20$               | mV                  |
|            |  | Over temp.   |        | $\pm 9$             | $\pm 15$              |        |           | $\pm 40$               | mV                  |
| $V_{CM}$   | Input common-mode voltage range <sup>3</sup> | $T_{amb} = 25\text{ }^\circ\text{C}$   | 0      |                     | $V_+ - 1.5$           | 0      |           | $V_+ - 1.5$            | V                   |
|            |  | Over temp.   | 0      |                     | $V_+ - 2.0$           | 0      |           | $V_+ - 2.0$            | V                   |
| $V_{IDR}$  | Differential input voltage <sup>1</sup>      | Keep all $V_{IN} \geq 0 V_{DC}$ (or $V_-$ if needed)   |        |                     | $V_+$                 |        |           | $V_+$                  | V                   |
| $I_{BIAS}$ | Input bias current <sup>4</sup>              | $I_{IN(+)}$ or $I_{IN(-)}$ with output in linear range<br>$T_{amb} = 25\text{ }^\circ\text{C}$   |        | 25                  | 250                   |        | 25        | 500                    | nA                  |
|            |  | Over temp.   |        | 200                 | 500                   |        |           | 1000                   | nA                  |
| $I_{OS}$   | Input offset current                         | $I_{IN(+)} - I_{IN(-)}$<br>$T_{amb} = 25\text{ }^\circ\text{C}$<br>Over temp.  |        | $\pm 5$<br>$\pm 50$ | $\pm 50$<br>$\pm 200$ |        | $\pm 5$   | $\pm 100$<br>$\pm 300$ | nA<br>nA            |
| $I_{OL}$   | Output sink current                          | $V_{IN(-)} \geq 1 V_{DC}$ ; $V_{IN(+)} = 0$ ;<br>$V_O \leq 1.5 V_{DC}$ ; $T_{amb} = 25\text{ }^\circ\text{C}$  | 6.0    | 16                  |                       | 6      | 16        |                        | mA                  |
|            | Output leakage current                       | $V_{IN(+)} \geq 1 V_{DC}$ ; $V_{IN(-)} = 0$<br>$V_O = 5 V_{DC}$ ; $T_{amb} = 25\text{ }^\circ\text{C}$<br>$V_O = 30 V_{DC}$ ; Over temp.                   |        | 0.1                 | 1.0                   |        | 0.1       | 1.0                    | nA<br>$\mu\text{A}$ |
| $I_{CC}$   | Supply current                               | $R_L = \infty$ on all comparators,<br>$T_{amb} = 25\text{ }^\circ\text{C}$   |        | 0.8                 | 2.0                   |        |           |                        |                     |
|            |  | $R_L = \infty$ on all comparators,<br>$V_+ = 30\text{ V}$  |        | 1.0                 | 2.5                   |        |           |                        | mA                  |
|            |  |  |        |                     |                       |        | 0.8       | 1.8                    | mA                  |
| $A_V$      | Voltage gain                                 | $R_L \geq 15\text{ k}\Omega$ ; $V_+ = 15 V_{DC}$   | 25     | 100                 |                       | 2      | 100       |                        | V/mV                |
| $V_{OL}$   | Saturation voltage                           | $V_{IN(-)} \geq 1 V_{DC}$ ; $V_{IN(+)} = 0$ ; $I_{SINK} \leq 4\text{ mA}$<br>$T_{amb} = 25\text{ }^\circ\text{C}$  |        | 400                 | 400                   |        | 150       | 400                    | mV<br>mV            |
|            |  | Over temp.   |        | 400                 | 700                   |        |           | 700                    |                     |
| $t_{LSR}$  | Large-signal response time                   | $V_{IN} = \text{TTL logic swing}$ ;<br>$V_{REF} = 1.4 V_{DC}$ ; $V_{RL} = 5 V_{DC}$ ;<br>$R_L = 5.1\text{ k}\Omega$ ; $T_{amb} = 25\text{ }^\circ\text{C}$ |        | 300                 |                       |        | 300       |                        | ns                  |
| $t_R$      | Response time <sup>5</sup>                   | $V_{RL} = 5 V_{DC}$ ; $R_L = 5.1\text{ k}\Omega$ ;<br>$T_{amb} = 25\text{ }^\circ\text{C}$   |        | 1.3                 |                       |        | 1.3       |                        | $\mu\text{s}$       |

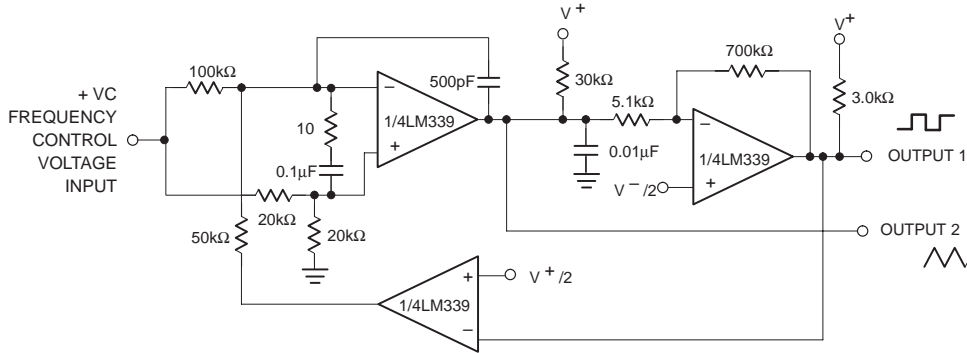
**NOTES:**

- Positive excursions of input voltage may exceed the power supply level by 17 V. As long as the other voltage remains within the common-mode range, the comparator will provide a proper output state. The low input voltage state must not be less than  $-0.3 V_{DC}$  (or  $0.3 V_{DC}$  below the magnitude of the negative power supply, if used).
- At output switch point,  $V_O \approx 1.4 V_{DC}$ ,  $R_S = 0\ \Omega$  with  $V_+$  from  $5 V_{DC}$  to  $30 V_{DC}$ ; and over the full input common-mode range ( $0 V_{DC}$  to  $V_+ - 1.5 V_{DC}$ ). Inputs of unused comparators should be grounded.
- The input common-mode voltage or either input signal voltage should not be allowed to go negative by more than 0.3 V. The upper end of the common-mode voltage range is  $V_+ - 1.5\text{ V}$ , but either or both inputs can go to  $30 V_{DC}$  without damage.
- The direction of the input current is out of the IC due to the PNP input stage. This current is essentially constant, independent of the state of the output so no loading change exists on the reference or input lines.
- The response time specified is for a 100 mV input step with a 5 mV overdrive. For larger overdrive signals, 300 ns can be obtained (see Figure 4, Typical performance characteristics).

# Quad voltage comparator

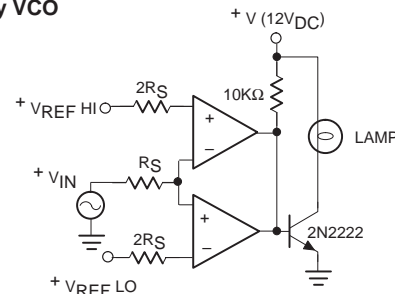
## LM139/239/239A/339/339A/ LM2901/MC3302

### EQUIVALENT CIRCUIT

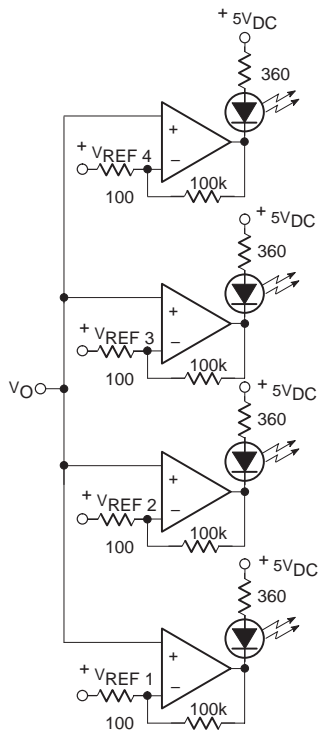


**NOTES:**  
 $V^+ = 30V_{DC}$   
 $+250mV_{DC} \leq V_C \leq 50V_{DC}$   
 $700H \leq f_O = 100kHz$

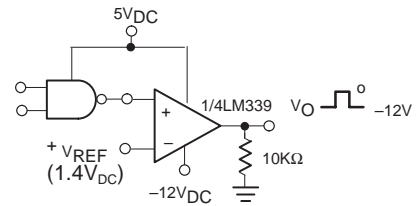
**Two-Decade High Frequency VCO**



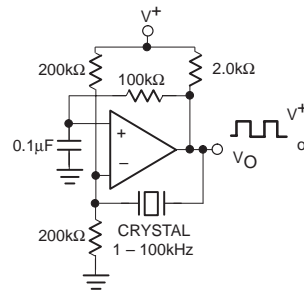
**Limit Comparator**



**Visible Voltage Indicator**



**TTL-to-MOS Logic Converter**



**Crystal-Controlled Oscillator**

**NOTE:**  
 Input of unused comparators should be grounded.

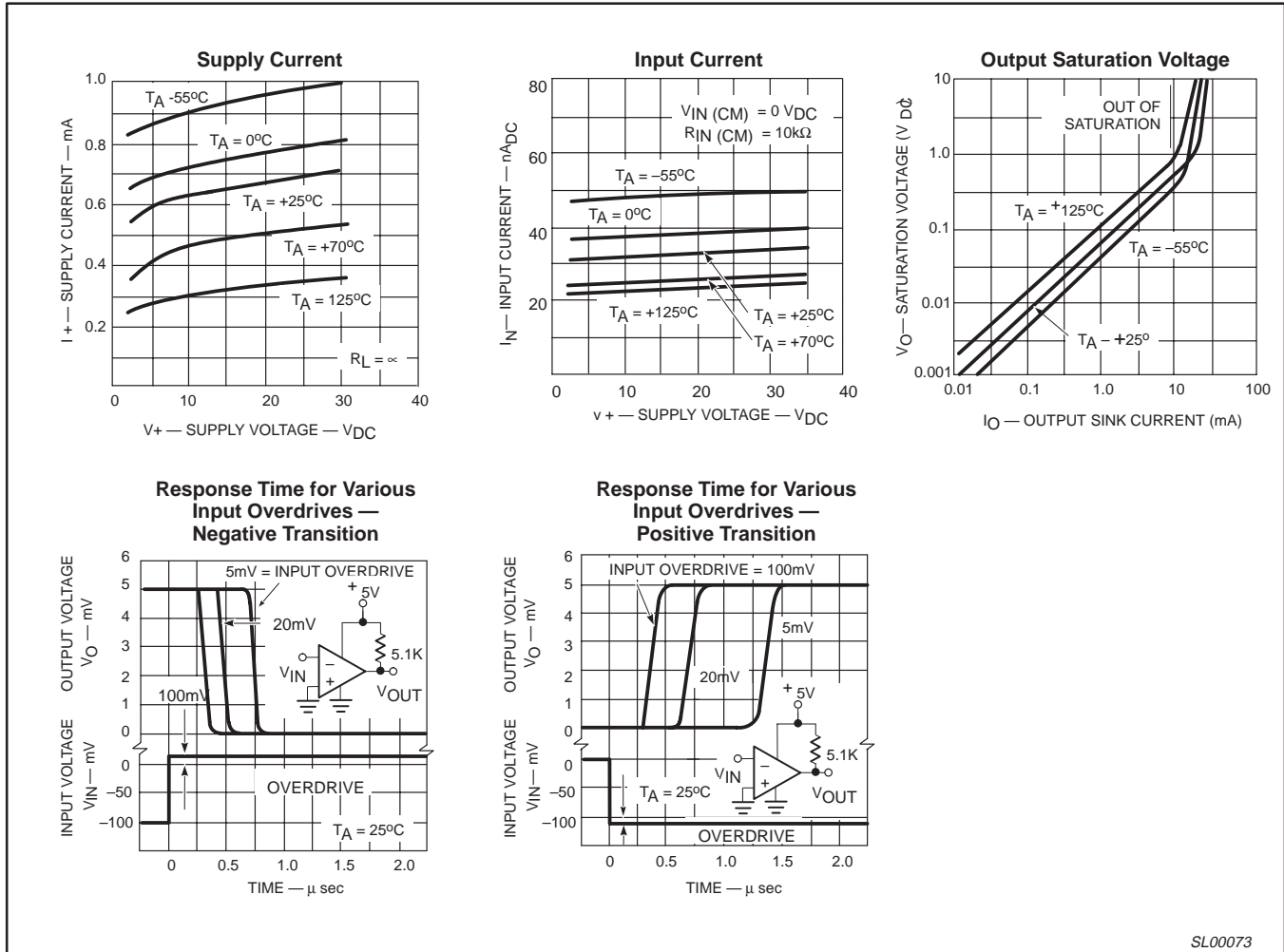
SL00072

**Figure 3. Equivalent circuit.**

# Quad voltage comparator

LM139/239/239A/339/339A/  
LM2901/MC3302

## TYPICAL PERFORMANCE CHARACTERISTICS



SL00073

Figure 4. Typical performance characteristics.

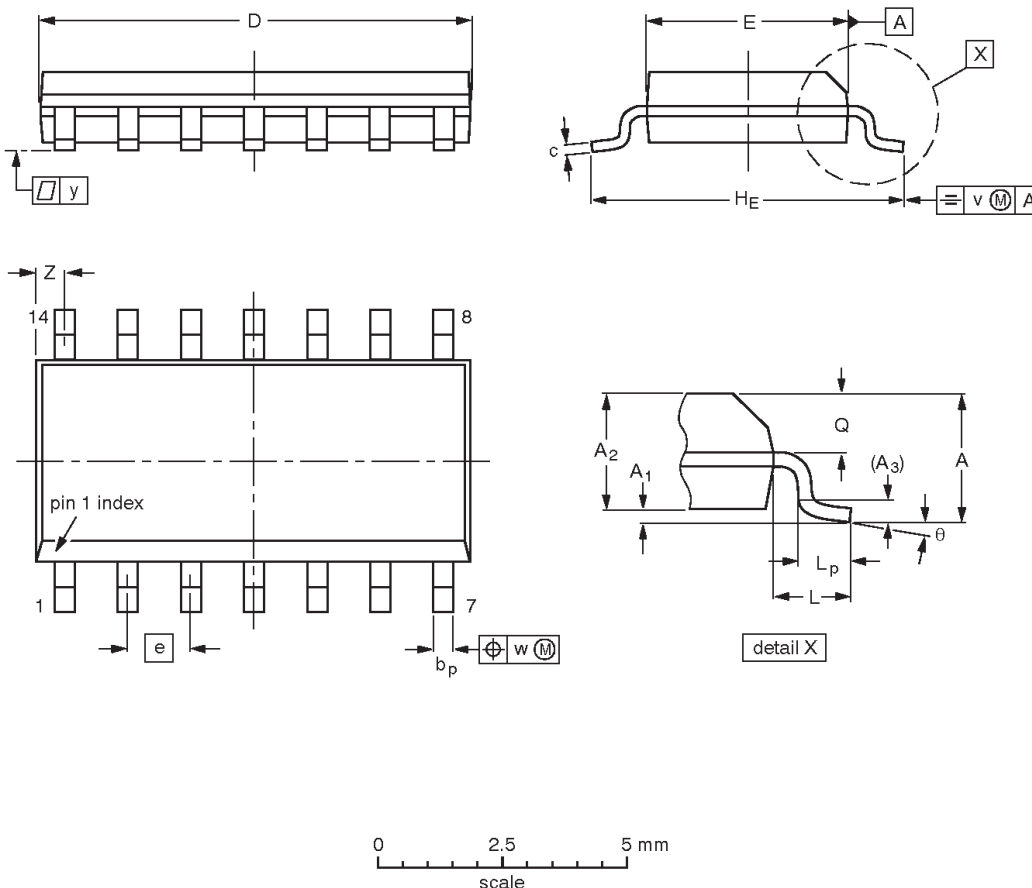


# Quad voltage comparator

LM139/239/239A/339/339A/  
LM2901/MC3302

SO14: plastic small outline package; 14 leads; body width 3.9 mm

SOT108-1



**DIMENSIONS (inch dimensions are derived from the original mm dimensions)**

| UNIT   | A max. | A <sub>1</sub> | A <sub>2</sub> | A <sub>3</sub> | b <sub>p</sub> | c                | D <sup>(1)</sup> | E <sup>(1)</sup> | e     | H <sub>E</sub> | L     | L <sub>p</sub> | Q              | v    | w    | y     | Z <sup>(1)</sup> | θ        |
|--------|--------|----------------|----------------|----------------|----------------|------------------|------------------|------------------|-------|----------------|-------|----------------|----------------|------|------|-------|------------------|----------|
| mm     | 1.75   | 0.25<br>0.10   | 1.45<br>1.25   | 0.25           | 0.49<br>0.36   | 0.25<br>0.19     | 8.75<br>8.55     | 4.0<br>3.8       | 1.27  | 6.2<br>5.8     | 1.05  | 1.0<br>0.4     | 0.7<br>0.6     | 0.25 | 0.25 | 0.1   | 0.7<br>0.3       | 8°<br>0° |
| inches | 0.069  | 0.010<br>0.004 | 0.057<br>0.049 | 0.01           | 0.019<br>0.014 | 0.0100<br>0.0075 | 0.35<br>0.34     | 0.16<br>0.15     | 0.050 | 0.244<br>0.228 | 0.041 | 0.039<br>0.016 | 0.028<br>0.024 | 0.01 | 0.01 | 0.004 | 0.028<br>0.012   |          |

**Note**

1. Plastic or metal protrusions of 0.15 mm maximum per side are not included.

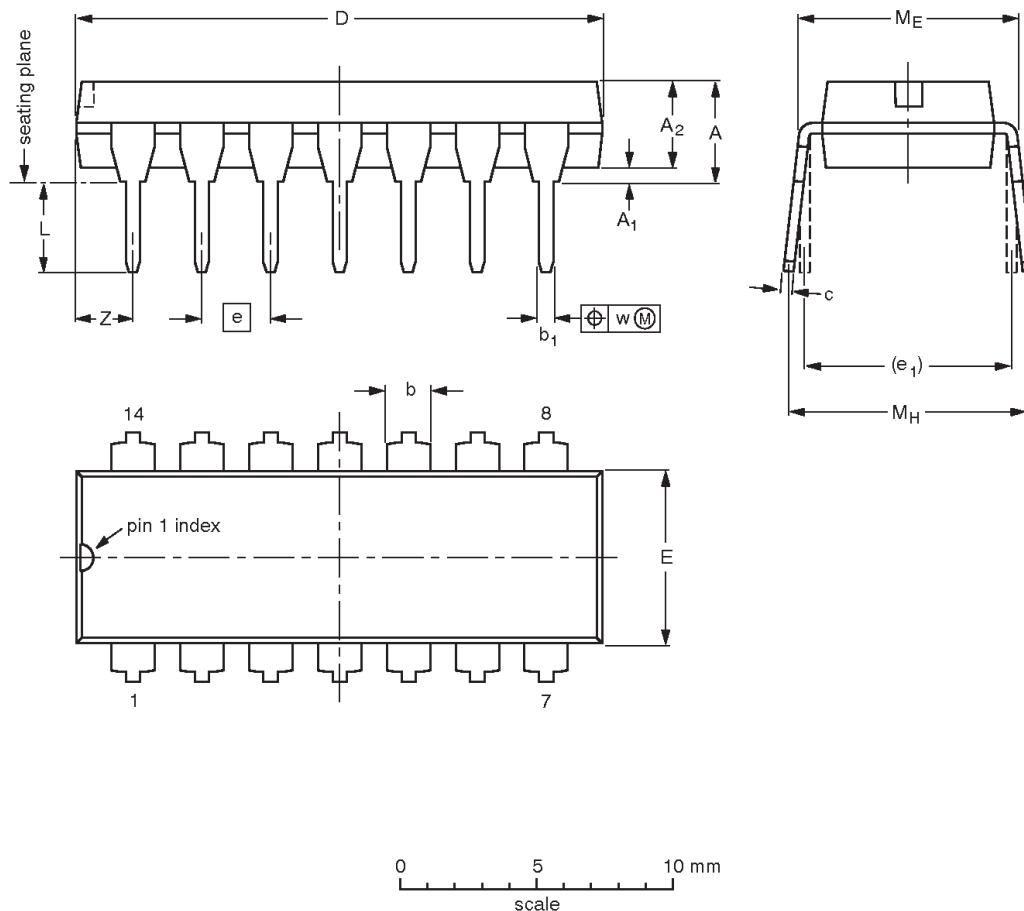
| OUTLINE VERSION | REFERENCES |        |      |  | EUROPEAN PROJECTION | ISSUE DATE           |
|-----------------|------------|--------|------|--|---------------------|----------------------|
|                 | IEC        | JEDEC  | EIAJ |  |                     |                      |
| SOT108-1        | 076E06     | MS-012 |      |  |                     | 97-05-22<br>99-12-27 |

Quad voltage comparator

LM139/239/239A/339/339A/  
LM2901/MC3302

DIP14: plastic dual in-line package; 14 leads (300 mil)

SOT27-1



DIMENSIONS (inch dimensions are derived from the original mm dimensions)

| UNIT   | A max. | A <sub>1</sub> min. | A <sub>2</sub> max. | b              | b <sub>1</sub> | c              | D <sup>(1)</sup> | E <sup>(1)</sup> | e    | e <sub>1</sub> | L            | M <sub>E</sub> | M <sub>H</sub> | w     | Z <sup>(1)</sup> max. |
|--------|--------|---------------------|---------------------|----------------|----------------|----------------|------------------|------------------|------|----------------|--------------|----------------|----------------|-------|-----------------------|
| mm     | 4.2    | 0.51                | 3.2                 | 1.73<br>1.13   | 0.53<br>0.38   | 0.36<br>0.23   | 19.50<br>18.55   | 6.48<br>6.20     | 2.54 | 7.62           | 3.60<br>3.05 | 8.25<br>7.80   | 10.0<br>8.3    | 0.254 | 2.2                   |
| inches | 0.17   | 0.020               | 0.13                | 0.068<br>0.044 | 0.021<br>0.015 | 0.014<br>0.009 | 0.77<br>0.73     | 0.26<br>0.24     | 0.10 | 0.30           | 0.14<br>0.12 | 0.32<br>0.31   | 0.39<br>0.33   | 0.01  | 0.087                 |

Note

1. Plastic or metal protrusions of 0.25 mm maximum per side are not included.

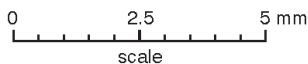
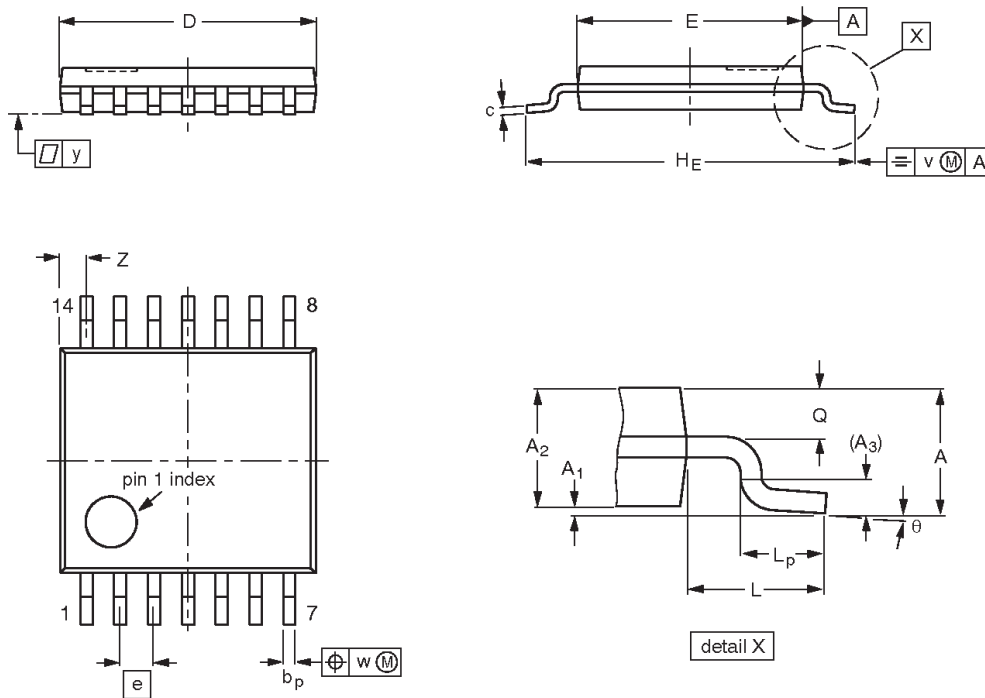
| OUTLINE VERSION | REFERENCES |        |           |  | EUROPEAN PROJECTION | ISSUE DATE           |
|-----------------|------------|--------|-----------|--|---------------------|----------------------|
|                 | IEC        | JEDEC  | EIAJ      |  |                     |                      |
| SOT27-1         | 050G04     | MO-001 | SC-501-14 |  |                     | 95-03-11<br>99-12-27 |

Quad voltage comparator

LM139/239/239A/339/339A/  
LM2901/MC3302

TSSOP14: plastic thin shrink small outline package; 14 leads; body width 4.4 mm

SOT402-1



**DIMENSIONS (mm are the original dimensions)**

| UNIT | A max. | A <sub>1</sub> | A <sub>2</sub> | A <sub>3</sub> | b <sub>p</sub> | c          | D <sup>(1)</sup> | E <sup>(2)</sup> | e    | H <sub>E</sub> | L   | L <sub>p</sub> | Q          | v   | w    | y   | Z <sup>(1)</sup> | θ        |
|------|--------|----------------|----------------|----------------|----------------|------------|------------------|------------------|------|----------------|-----|----------------|------------|-----|------|-----|------------------|----------|
| mm   | 1.10   | 0.15<br>0.05   | 0.95<br>0.80   | 0.25           | 0.30<br>0.19   | 0.2<br>0.1 | 5.1<br>4.9       | 4.5<br>4.3       | 0.65 | 6.6<br>6.2     | 1.0 | 0.75<br>0.50   | 0.4<br>0.3 | 0.2 | 0.13 | 0.1 | 0.72<br>0.38     | 8°<br>0° |

**Notes**

1. Plastic or metal protrusions of 0.15 mm maximum per side are not included.
2. Plastic interlead protrusions of 0.25 mm maximum per side are not included.

| OUTLINE VERSION | REFERENCES |        |      |  | EUROPEAN PROJECTION | ISSUE DATE            |
|-----------------|------------|--------|------|--|---------------------|-----------------------|
|                 | IEC        | JEDEC  | EIAJ |  |                     |                       |
| SOT402-1        |            | MO-153 |      |  |                     | -95-04-04<br>99-12-27 |

## Quad voltage comparator

LM139/239/239A/339/339A/  
LM2901/MC3302

## Data sheet status

| Data sheet status <sup>[1]</sup> | Product status <sup>[2]</sup> | Definitions  |
|----------------------------------|-------------------------------|--|
| Objective data                   | Development                   | This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.  |
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[2] The product status of the device(s) described in this data sheet may have changed since this data sheet was published. The latest information is available on the Internet at URL <http://www.semiconductors.philips.com>.

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